

PE-6S

PICMG 1.3 6 Slots Backplane

with PCI-E X16, PCI-E X4, PCI-E X1 and PCI Slots

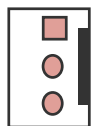
Quick Installation Guide

Version 1.0

Apr. 01, 2006

Jumpers setting

CN1
(ATX 5VSB/PS on Pin Header)



1 : GND
2 : PS_ON-
3 : 5VSB

CN2

(3Pin SDVO Control Signal Pin Header)



1 : EXP_EN
2 : DVO_CLK
3 : SDVO_DAT

JP1

(Power Switch)



1 : PWR_SW
2 : GND

JP2(Reset)



1 : Reset
2 : GND

JP3

(PCIE Link Configurations)

Jumper short Pin1 and Pin2 : PCIE2 ~ PCIE3 = x1
Jumper short Pin2 and Pin3 : PCIE2 = Not available
PCIE3 = x4

JP3	PCIE2	PCIE3
1-2	PCIEx1	PCIEx1
2-3	N/A	PCIEx4



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